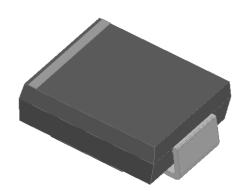




Surface Mount High Efficient Rectifier





Features

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- High forward surge capability
- Super fast reverse recovery time
- \bullet Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^{\circ}\text{C}$

Typical Applications

For use in high frequency rectification of power supplies, inverters, converters, and freewheeling diodes for consumer and telecommunication.

Mechanical Data

• Package: DO-214AB (SMC)

Molding compound meets UL 94 V-0 flammability rating,

RoHS-compliant, halogen-free

• Terminals: Tin plated leads, solderable per

J-STD-002 and JESD22-B102

• Polarity: Color band denotes the cathode end

■Maximum Ratings (Ta=25°C Unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | HS5A | HS5B | HS5D | HS5F | HS5G | HS5J | HS5K | HS5M |
|---|------------------|------------------|------|------|------|-------|------|------|------|------|
| Device marking code | | | HS5A | HS5B | HS5D | HS5F | HS5G | HS5J | HS5K | HS5M |
| Maximum Repetitive Peak Reverse Voltage | VRRM | V | 50 | 100 | 200 | 300 | 400 | 600 | 800 | 1000 |
| Maximum RMS Voltage | VRMS | V | 35 | 70 | 140 | 210 | 280 | 420 | 560 | 700 |
| Maximum DC blocking Voltage | VDC | V | 50 | 100 | 200 | 300 | 400 | 600 | 800 | 1000 |
| Average Rectified Output Current @60Hz sine wave, Resistance load, TL (FIG.1) | Io | Α | 5.0 | | | | | | | |
| Forward Surge Current (Non-repetitive) @60Hz Half-sine wave,1 cycle, Tj=25°C | | Α | | | | 15 | 50 | | | |
| Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25°C | I _{FSM} | IFSM A | 300 | | | | | | | |
| Current squared time @1ms≤t≤8.3ms Tj=25°C | l²t | A ² s | | | | 9 | 4 | | | |
| Storage Temperature | T _{stg} | $^{\circ}$ | | | | -55 ~ | +150 | | | |
| Junction Temperature | Tj | °C | | | | -55 ~ | +150 | | | |

■Electrical Characteristics (T_a=25°C Unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | TEST CONDITIONS | HS5A | HS5B | HS5D | HS5F | HS5G | HS5J | HS5K | HS5M |
|---------------------------------------|-----------------|------|---|------|------|------|------|------|------|------|------|
| Maximum instantaneous forward voltage | VF | ٧ | IFM=5.0A | 1.0 | | 1.3 | | 1.7 | | | |
| Maximum reverse recovery time | t _{rr} | ns | I _F =0.5A,I _R =1.0A, I _n =0.25A | 50 | | | | 75 | | | |
| Maximum DC reverse current at | t IR | | T _j =25°C 5 | | | | 5 | | | | |
| rated DC blocking voltage | ır. | μA | T _j =125°C | | | | 10 | 00 | | | |
| Typical junction capacitance | Cj | pF | Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C | | 72 | | 4 | 6 | | 52 | |

HS5A THRU HS5M

■Thermal Characteristics $(T_a=25$ $^{\circ}$ Unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | HS5A | HS5B | HS5D | HS5F | HS5G | HS5J | HS5K | HS5M |
|----------------------------|-----------------------|------|------|------|------|------|------|------|------|------|
| | RθJ-A(1) | | 48 | | | | | | | |
| Typical Thermal resistance | RθJ-L(1) | °C/W | 15 | | | | | | | |
| | R ₀ J-C(1) | | 12 | | | | | | | |

Note(1)

Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.6" x 0.6" (16 mm x 16 mm) copper pad areas

■Ordering Information (Example)

| PREFERED P/N | PACKAGE CODE | UNIT WEIGHT(g) | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|--------------|-----------------|-------------------|-------------------------|-------------------------|----------------------------|------------------|
| HS5A~HS5M | F1 | Approximate 0.254 | 3000 | 1 | 42000 | 13" reel |

■ Characteristics(Typical)



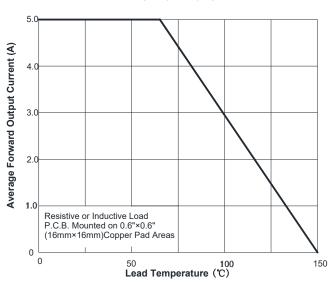


FIG.2: Forward Surge Current Capability

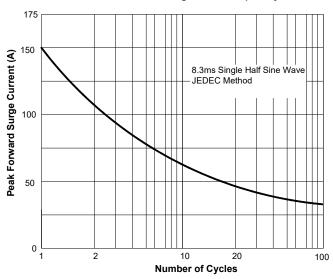


FIG.3: Typical Forward Voltage

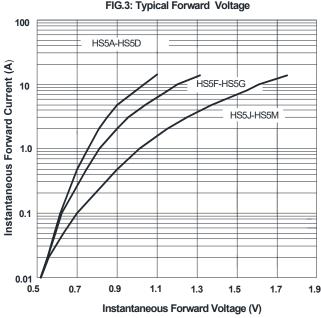
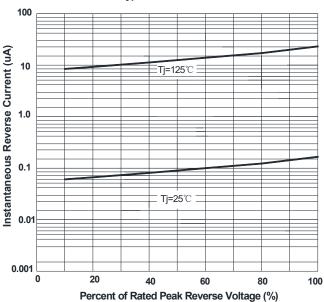


FIG.4: Typical Reverse Characteristics



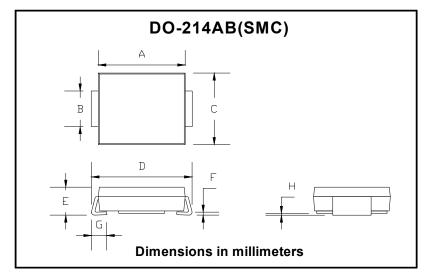
2/4

HS5A THRU HS5M

10 Ω NONINDUCTIVE 50 Ω trr NONINDUCTIVE +0.5A **▼** DUT PULSE 0 GENERATOR -0.25A (NOTE2) OSCILLOSCOPE (NOTE1) NOTES: -1.0A 1.Rise Time=7ns max .Inpot Impedance=1M Ω 22pf 2.Rise Time=10ns max.Sourse Impedance=50 Ω SET TIME BASE FOR 5/10ns/cm

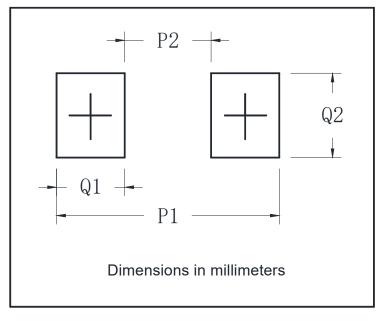
FIG.5: Diagram of circuit and Testing wave form of reverse recovery time

■ Outline Dimensions



| DO-214AB (SMC) | | | | | |
|----------------|------|------|--|--|--|
| Dim | Min | Max | | | |
| Α | 6.60 | 7.11 | | | |
| В | 2.85 | 3.27 | | | |
| С | 5.59 | 6.22 | | | |
| D | 7.75 | 8.13 | | | |
| E | 1.99 | 2.61 | | | |
| F | 0.15 | 0.31 | | | |
| G | 0.76 | 1.52 | | | |
| Н | 0.05 | 0.20 | | | |

■ Suggested pad layout



| DO-214AB (SMC) | | | | | |
|----------------|------|--|--|--|--|
| Dim | Min | | | | |
| P1 | 9.9 | | | | |
| P2 | 3.84 | | | | |
| Q1 | 3.03 | | | | |
| Q2 | 3.82 | | | | |

3/4



HS5A THRU HS5M

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